

IN THE CLAIMS

Please amend the claims as follows:

1. (Currently amended) A bias potential generating apparatus for generating ~~a plurality of potentials as bias potentials and generating each of the potentials by switching between a bias potential and a standby potential~~ in a power save mode and a bias potential in an operation mode, and for selectively outputting the standby potential or the bias potential to an output node, comprising:

a bias potential outputting circuit for preliminarily determining a level of the bias potential and for being capable of outputting the bias potential to the output node;

a standby potential outputting circuit for preliminarily determining a level of the standby potential and for being capable of outputting the standby potential to the output node;

a potential restoration circuit for outputting a restoring the generated potential having a level different from that of the standby potential to the output node for a predetermined time period at the timing of transfer from power save mode to operation mode ~~bias potential~~; and

a drive control circuit connected to ~~for controlling the drive operation of the potential restoration circuit~~ and capable of changing the predetermined time period [[:]]

~~wherein the potential restoration circuit is provided for each of the generated potential.~~

2. (Original) A bias potential generating apparatus according to claim 1, wherein the drive control circuit controls the drive operation of the potential restoration circuit based on a pulse waveform signal, the drive control circuit including an oscillation circuit for generating a clock signal for generating the pulse waveform signal.

3. (Original) A bias potential generating apparatus according to claim 1, further comprising:

a drive time setting circuit for setting the drive time of the potential restoration circuit arbitrarily.

4. (Original) A bias potential generating apparatus according to claim 3, wherein the drive control circuit controls the drive operation of the potential restoration circuit based on a pulse waveform signal; and

wherein the drive time setting circuit is a register for outputting a set value equal to the pulse width of the pulse waveform signal.

5. (Original) A bias potential generating apparatus according to claim 1, further comprising: a current setting circuit for arbitrarily setting a restoration current used in the potential restoration circuit at the time of restoring the bias potential.

6. (Original) A bias potential generating apparatus according to claim 5, wherein the potential restoration circuit is constituted by a plurality of MOS transistors connected in parallel to each other for generating the restoration current; and

wherein the current setting circuit supplies a conduction control signal individually to each of the plurality of the MOS transistors.

7. (Original) A bias potential generating apparatus according to claim 1, wherein the potential restoration circuit includes a plurality of MOS transistors connected in series with each other for generating a threshold voltage by shorting the gate potential and the drain potential; and

wherein the restoration voltage of the potential restoration circuit used for restoring the bias potential can be set arbitrarily by changing the number of the MOS transistors.

8. (Original) A bias potential generating apparatus according to claim 1, further comprising:

a reference potential generating circuit for generating a reference potential having a potential equivalent to the bias potential;

wherein the drive control circuit drives the potential restoration circuit during the period when the generated potential fails to reach the reference potential, and stops driving the potential restoration circuit at the time point when the generated potential reaches the reference potential.

9. (Original) A bias potential generating apparatus according to claim 1, further comprising:

a restoration potential generating circuit for generating a restoration potential equivalent to the bias potential;

wherein the potential restoration circuit shorts the generated potential to the restoration potential at the time of restoring the potential.

10-13. (Cancelled)

14. (Original) A bias potential generating apparatus for generating a potential by switching between a bias potential and a standby potential, comprising:

a potential restoration circuit for restoring the generated potential from the standby potential to the bias potential; and

a drive control circuit for controlling the drive operation of the potential restoration circuit;

wherein the potential restoration circuit includes a plurality of MOS transistors connected in series with each other for generating a threshold voltage by shorting the gate potential and the drain potential; and

wherein the restoration voltage used for restoring the bias potential can be arbitrarily set by changing the number of the MOS transistors of the potential restoration circuit.

15-22. (Cancelled)

23. (New) A bias potential generating apparatus according to claim 1, wherein the potential restoration circuit includes a plurality of MOS transistors connected in series with each other for generating a threshold voltage by shorting the gate potential and the drain potential.